

FIG. 1

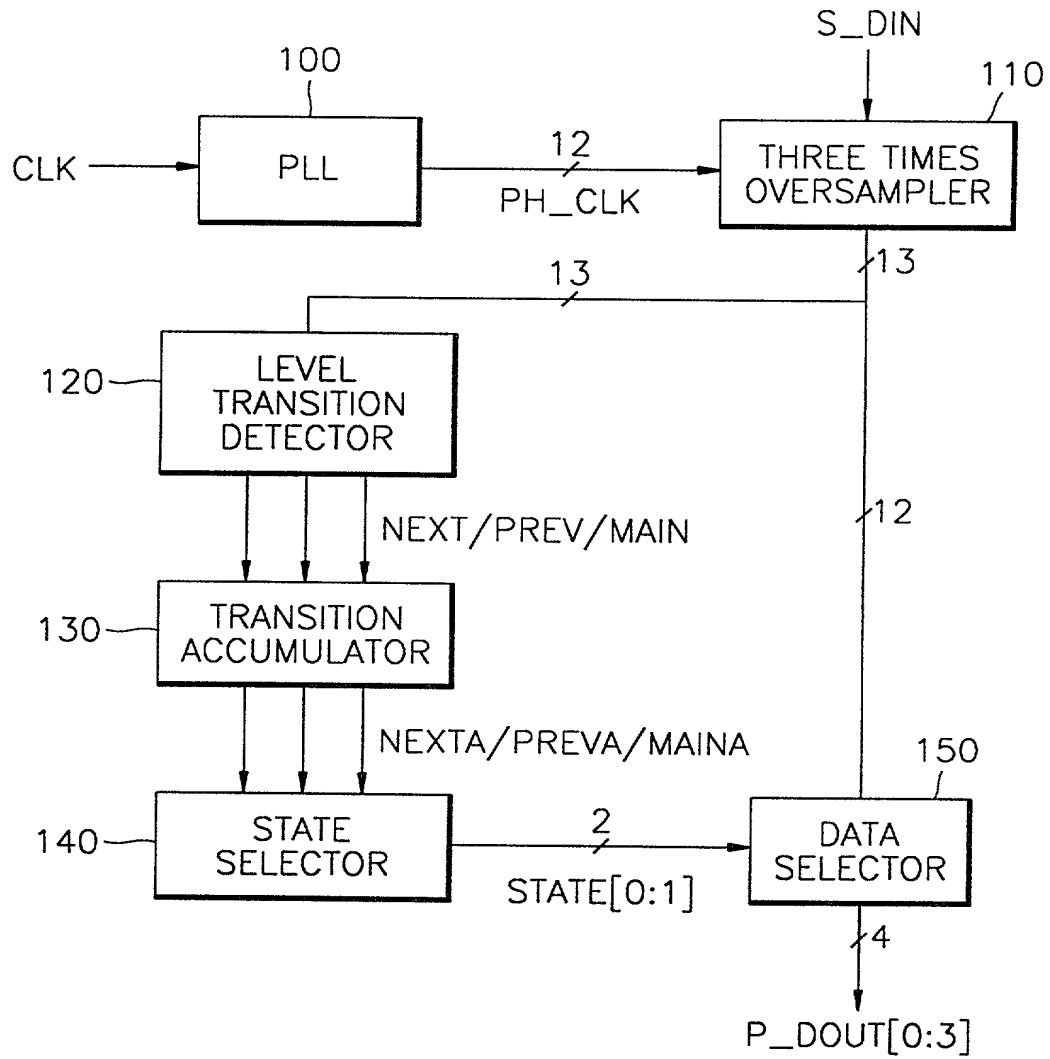


FIG. 2

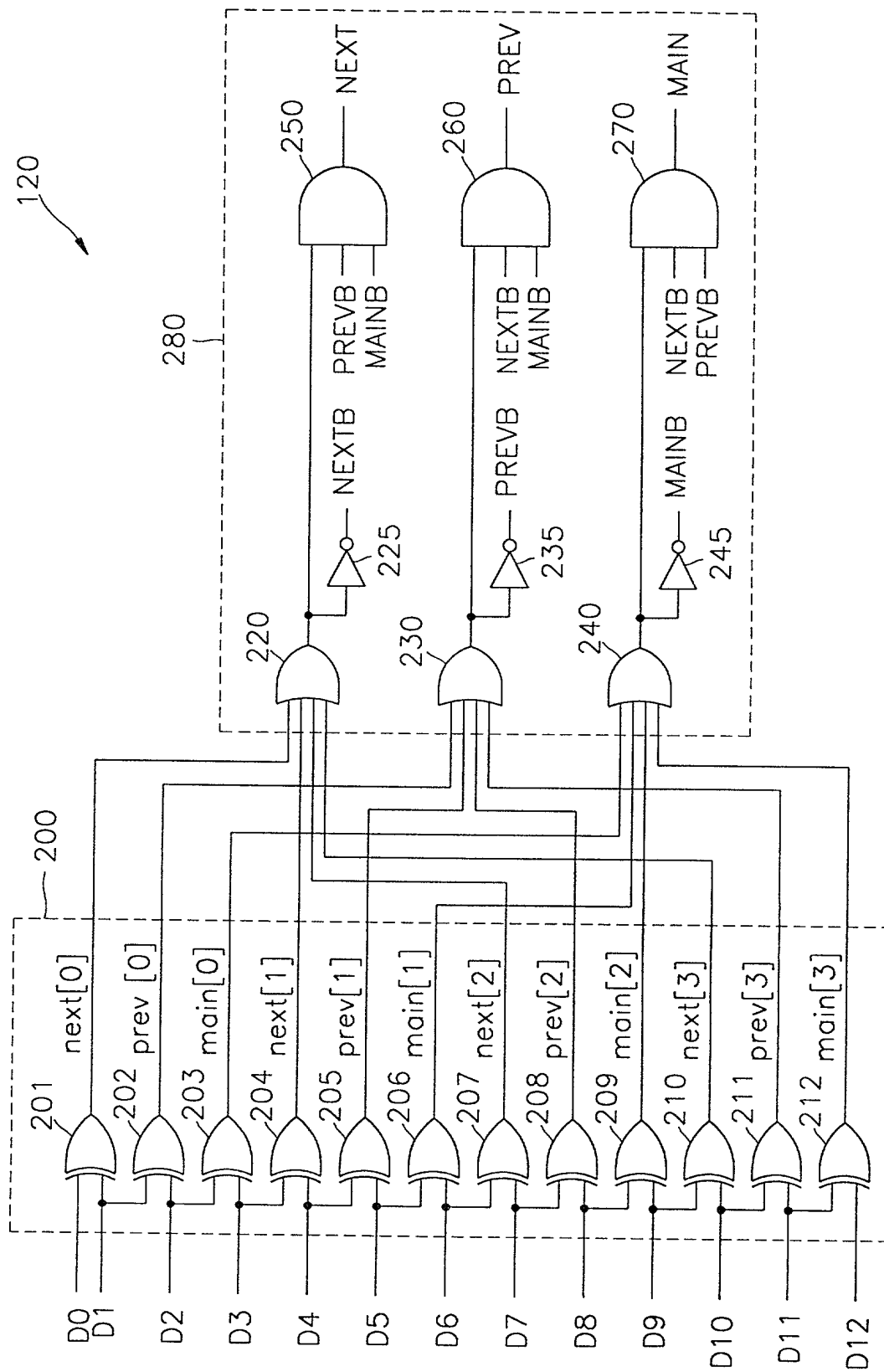


FIG. 3

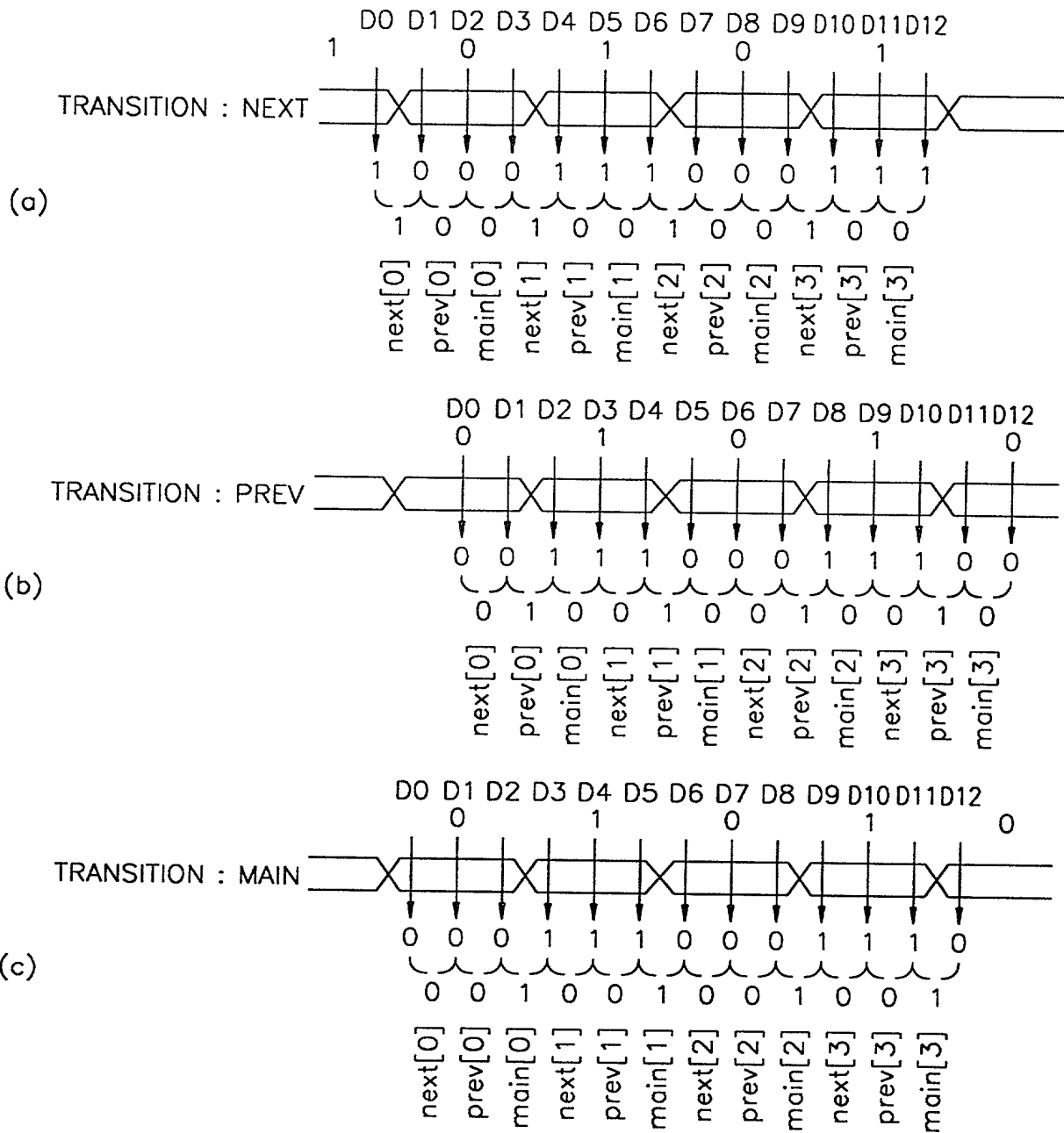


FIG. 4

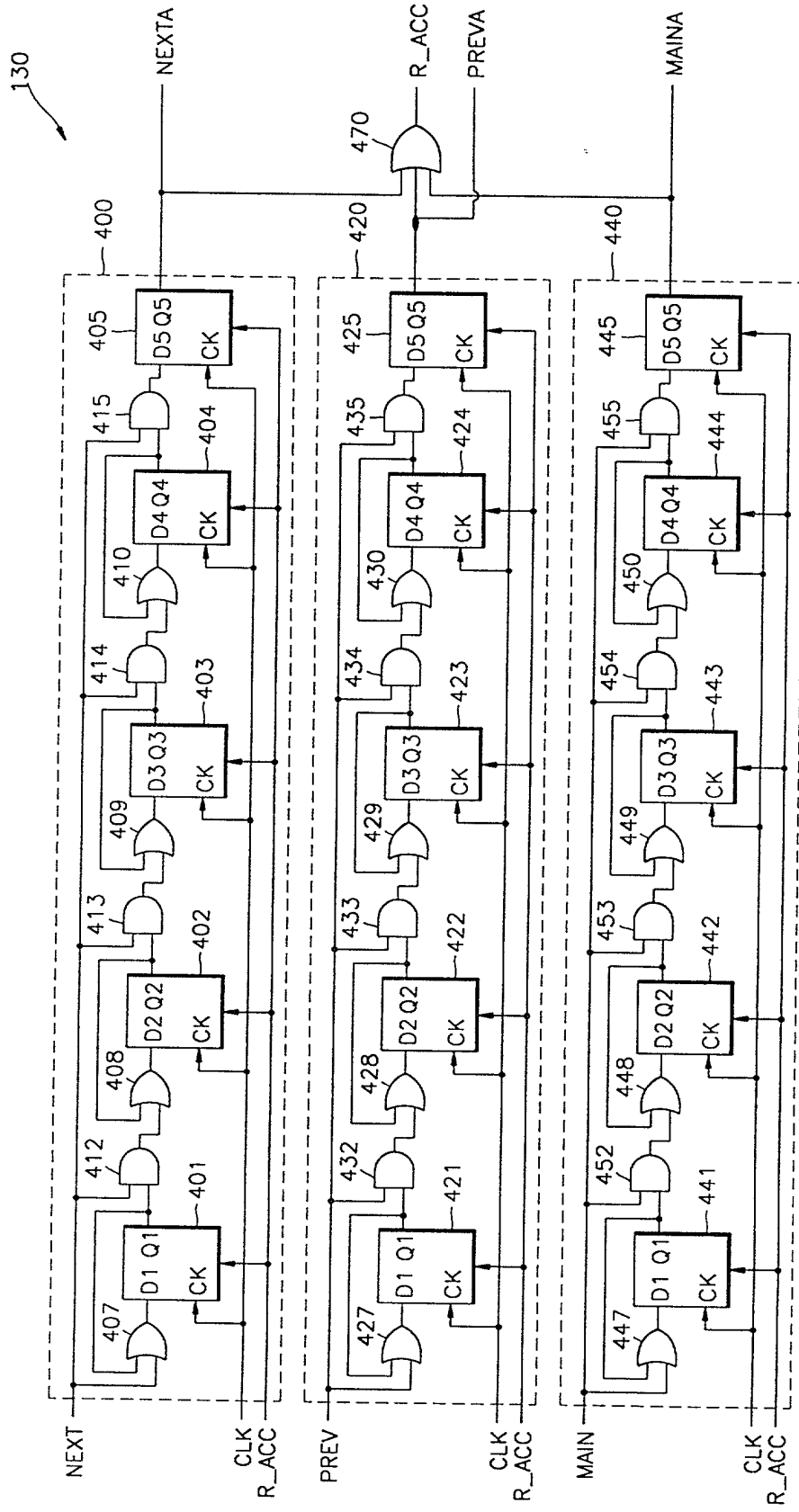


FIG. 5

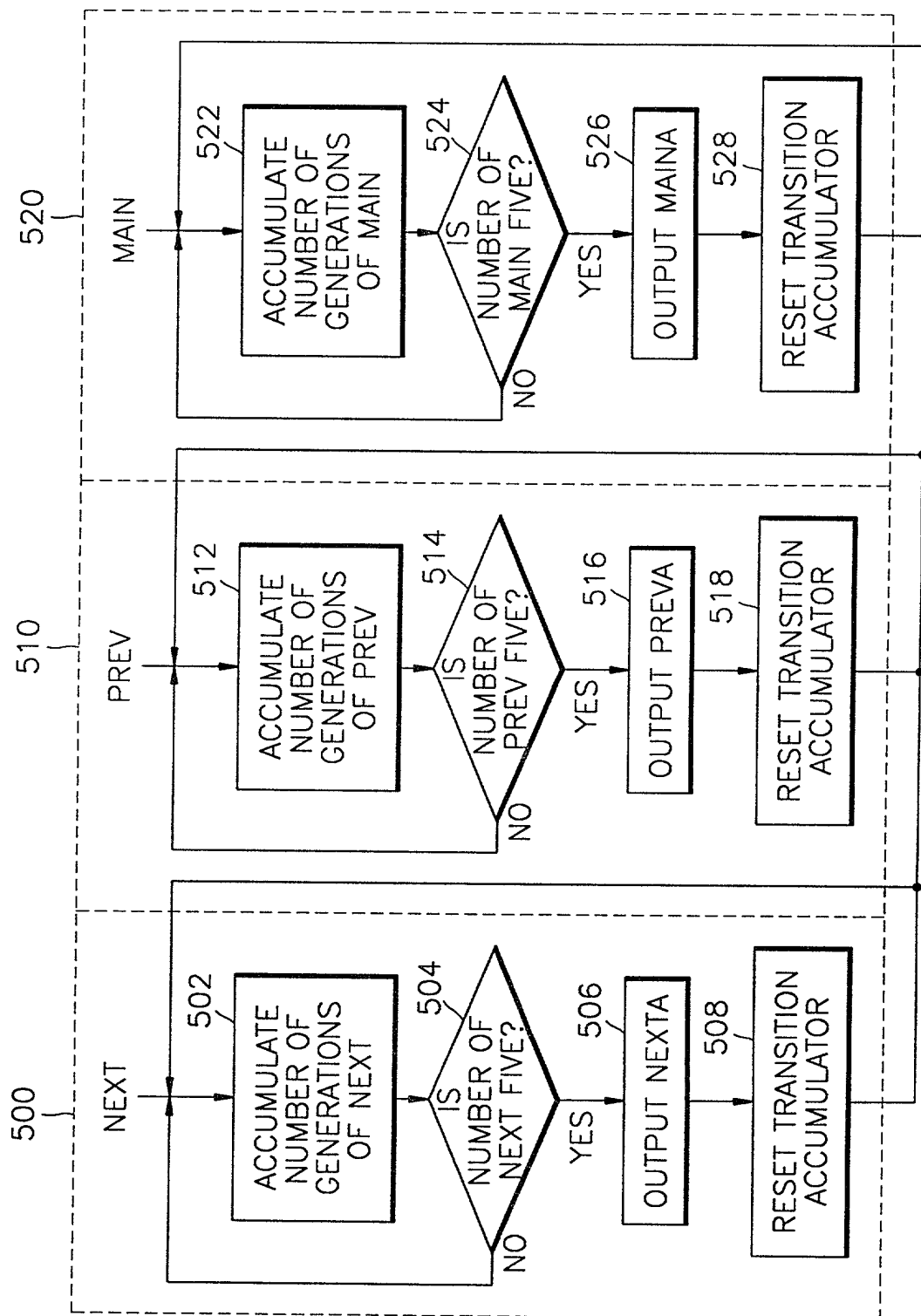


FIG. 6

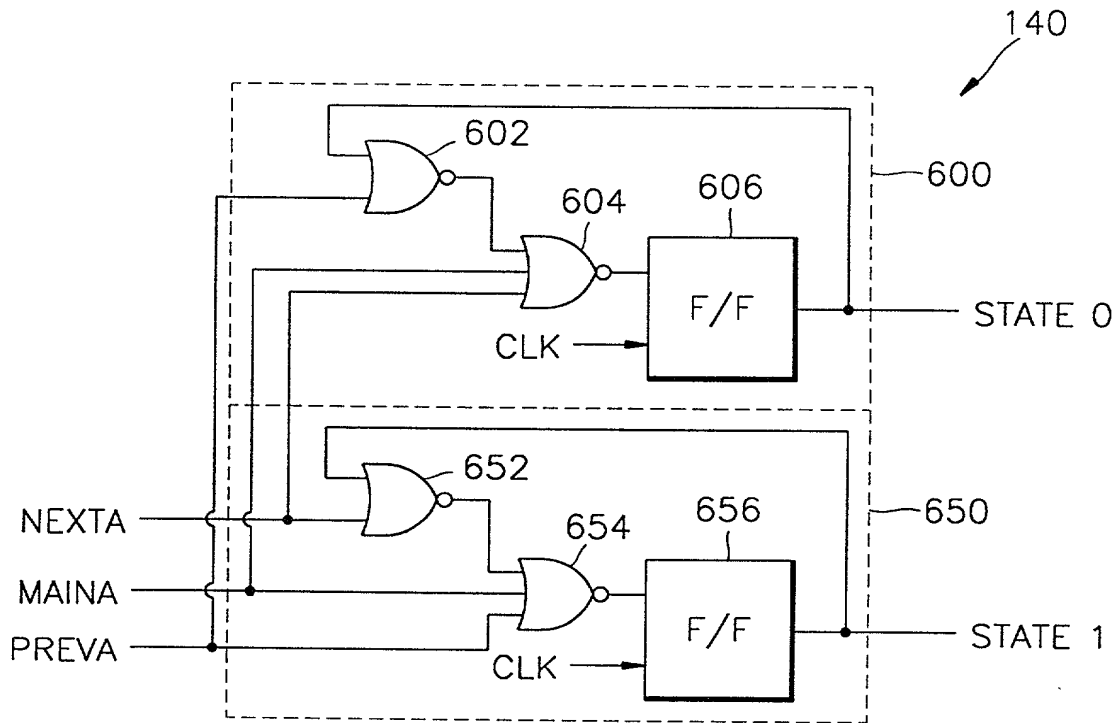


FIG. 7

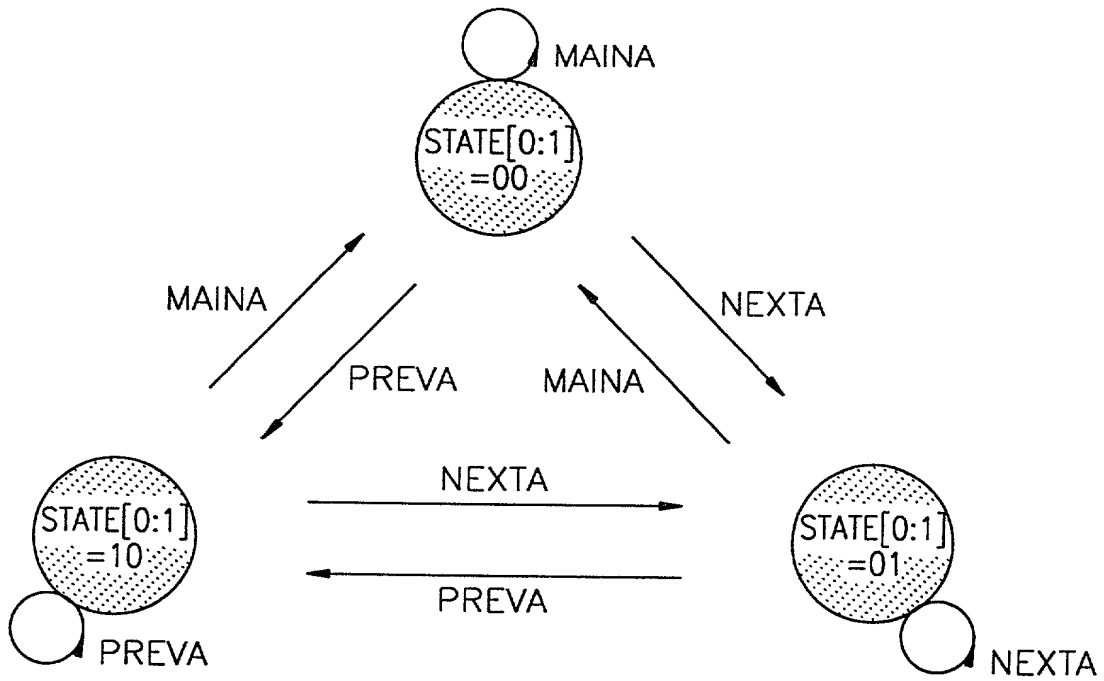


FIG. 8

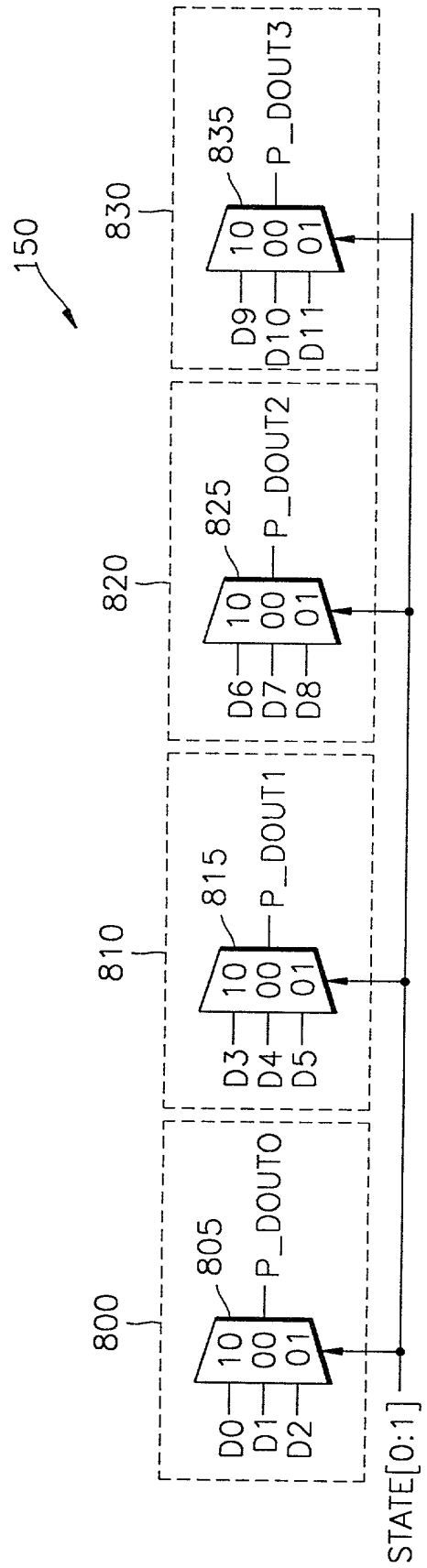


FIG. 9

STATE SIGNAL	INPUT MULTIPLEXER	OUTPUT MULTIPLEXER	OUTPUT ACCUMULATION
STATE[0:1]=00	D0~D11	D1, D4, D7, D10	MAINA
STATE[0:1]=10		D0, D3, D6, D9	PREVA
STATE[0:1]=01		D2, D5, D8, D11	NEXTA

FIG. 10

